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trigger integrator predetermined time threshold voltage conversion

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Multi-mode oscilloscope **trigger** with compensating **trigger** delay ...

The method of claim 1 wherein said **predetermined** sampling **time** intervals occur the transition of a **trigger threshold** 38, the differing **time** delay 40 and 42 between ... to convert the **time** duration into a **voltage** corresponding thereto. ... the analog **integrator** 66 and A/D **converter** 62 may be replaced with their ...

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5309357, **integration** and analog-to-digital **conversion** is carried out in one of ... a given **threshold voltage** level R using a comparator 18 and **voltage** reference 19. ... below the **predetermined threshold**, the control unit 20 resets **integrator** ... output **trigger** signal on line 27, the ADCs 26 convert the **integration** ...

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Aug 22, 2008 ... pulse **voltage** to a level that can **trigger** the next component. ... the input pulses to have a **voltage** higher than the **trigger threshold** of the 7406, ... the output will remain high for the **predetermined time** constant. ...

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Variable frequency current control device for discharge lamps - US ...

As intermediate current-to-**voltage conversion** could be used followed by a ... lamp current reaches a **predetermined** peak value, the **threshold** detector **triggers** a bistable ... input of an adder circuit 45 and to an input of an **integrator** circuit 46. ... At that time the **threshold** detector supplies a **trigger** pulse to ...

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ate from large split supplies, logic level **conversion**, linear op- eration, and special designs relying on a CMOS characteristic. when a slowly varying function exceeds a **predetermined** ence between its **threshold** and the input **voltage** V ... the reset time is much smaller than the **integration time**. ...

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High Precision **Time-to-Amplitude Converter** for Diffuse Optical ...

predetermined threshold indicating that an event takes place. Due to the random nature of the detector gain ... The CFD output pulse (START) **triggers** the ... source **voltage** of the reset transistor S3. The **integrator** ...

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MAPMT level **trigger** : Each **time** the signal coming from a MAPMT pixel exceeds ... The